

Non-isolated DC-DC converter
Ultra-wide input and buck single output



CE Report EN 62368-1 UKCA Report BS EN 62368-1 Patent Protection RoHS

FEATURES

- Ultra-wide input voltage range: 18 - 85VDC
- High efficiency up to 98%
- Input under-voltage protection, output short-circuit, over-current protection, over-temperature protection
- Operating ambient temperature range: -40℃ to +105℃
- Industry standard 1/8-Brick package and pin-out
- Maximum 360W output power

The KJB48xxEB(F)-10A series are high efficiency switching regulators. It features ultra-wide input range of 18- 85V, efficiency up to 98%, operating temperature of -40℃ to +105℃, input under-voltage protection, output short-circuit and output over-current protection, over-temperature protection, remote control, output voltage regulation and remote compensation and other functions. It is widely used in robotics, communications, battery management, DC-DC distributed power supply and other occasions.

Selection Guide

Certification	Part No. ^①	Input Voltage(VDC)		Output		Full Load Efficiency(%) Min./Typ. ^④	Capacitive Load (μF) Max.
		Nominal ^② (Range)	Max. ^③ (VDC)	Voltage (VDC)	Current(A) Max.		
EN/BS EN	KJB4805EB(F)-10A	48, 72	90	5	10	90/92	8500
	KJB4812EB(F)-10A	(18-85)		12	10	93/95	5500
	KJB4815EB(F)-10A	48, 72 (21-85)		15	10	93/95	4400
	KJB4824EB(F)-10A	48, 72 (30-85)		24	10	94/96	3300
	KJB4836EB(F)-10A	48, 72 (43-85)		36	10	96/98	1800

Notes:

① "F" means heat sink package; We recommend to choose modules with a heat sink for enhanced heat dissipation and applications with extreme temperature requirements;

② For input voltage exceeding 48VDC, an input capacitor of 330μF/100V is required;

③ The input voltage should not exceed this value, otherwise permanent and unrecoverable damage may be caused;

④ The above efficiency values are measured at nominal 48VDC input voltage.

Input Specifications

Item	Operating Conditions		Min.	Typ.	Max.	Unit
Input Current (full load / no-load)	Nominal 48VDC input	KJB4805EB(F)-10A	--	1133/2	1158/--	mA
		KJB4812EB(F)-10A	--	2632/2	2689/--	
		KJB4815EB(F)-10A	--	3290/2	3361/--	
		KJB4824EB(F)-10A	--	5209/2	5320/--	
		KJB4836EB(F)-10A	--	7654/2	7813/--	
Reverse Polarity at Input	Nominal 48VDC input		--	200	--	VDC
Surge Voltage (1sec. max.)			-0.7	--	90	
Start-up Voltage	KJB4805EB(F)-10A, KJB4812EB(F)-10A		--	--	18	
	KJB4815EB(F)-10A		--	--	21	
	KJB4824EB(F)-10A		--	--	30	
	KJB4836EB(F)-10A		--	--	43	

Under-voltage Protection	KJB4805EB(F)-10A, KJB4812EB(F)-10A	13	--	--	VDC
	KJB4815EB(F)-10A	16	--	--	
	KJB4824EB(F)-10A	24	--	--	
	KJB4836EB(F)-10A	36	--	--	
Input Filter		Capacitance filter			
Hot Plug		Unavailable			
Input Reverse Polarity Protection		Unavailable			
Ctrl*	Module on	Ctrl pin open or pulled high(TTL 3 - 20VDC)			
	Module off	Ctrl pin pulled low to -Vin(0 - 1VDC)			
	Input current when off	--	1	5	mA
Note: *The ctrl pin voltage is referenced to input -Vin.					

Output Specifications

Item	Operating Conditions	Min.	Typ.	Max.	Unit
Voltage Accuracy	Input voltage range, 10% - 100%Io	--	±2	±3	%
Linear Regulation	Full load, input voltage range	--	±0.3	±1	
Load Regulation	Nominal 48VDC input, 10% - 100%Io	--	±0.5	±2	
Transient Recovery Time	Nominal 48VDC input, 25% load step change	--	300	500	us
Transient Response Deviation	Nominal 48VDC input, 25% load step change	5VDC output	--	±8	%
		Others	--	±5	
Temperature Coefficient	Operating temperature -40℃ to +105℃, full load	--	±0.02	±0.03	%/℃
Ripple & Noise ^①	20MHz bandwidth, nominal 48VDC input, full load	5VDC, 12VDC, 15VDC output	--	200	mVp-p
		24VDC, 36VDC output	--	250	
Over-temperature Protection ^②	Maximum surface temperature of the product	--	125	--	℃
Over-current Protection	Normal temperature, input voltage range	110	130	230	%Io
Short-circuit Protection	Input voltage range	Hiccup, continuous, self-recovery			

Notes: ① The method is used for ripple and noise test, please refer to Fig.2 and DC-DC Converter Application Notes for specific information;
② Over-temperature protection for product is output off.

General Specifications

Item	Operating Conditions	Min.	Typ.	Max.	Unit
Isolation	Input/Output - Shell, Electric Strength Test for 1 minute with a leakage current of 1mA max.	1500	--	--	VDC
Trim ^①		90	--	110	%Vo
Sense ^①	See part of Remote Sense Application	--	--	105	
Operating Temperature		-40	--	+105	℃
Storage Temperature		-55	--	+125	
Pin Soldering Resistance Temperature	Wave-soldering, 10 second	--	--	260	
	Soldering spot is 1.5mm away from case for 10 seconds	--	--	300	
Storage Humidity	Non-condensing	5	--	95	%RH
Vibration		10-150Hz, 5g, 0.75mm, 90 Min. along X, Y and Z			
Switching Frequency ^②	Nominal 48VDC input, Full load	KJB4805EB(F)-10A	--	190	kHz
		KJB4812EB(F)-10A	--	330	
		KJB4815EB(F)-10A	--	390	
		KJB4824EB(F)-10A	--	270	
		KJB4836EB(F)-10A	--	200	
MTBF	MIL-HDBK-217F@25℃	1000	--	--	k hours

Notes: ① When using Trim and Sense, the 24VDC output needs to meet Vin≥34VDC, the 36VDC output needs to meet Vin≥48VDC;
② Switching frequency varies with input voltage and load, ranging from 110-750 kHz.

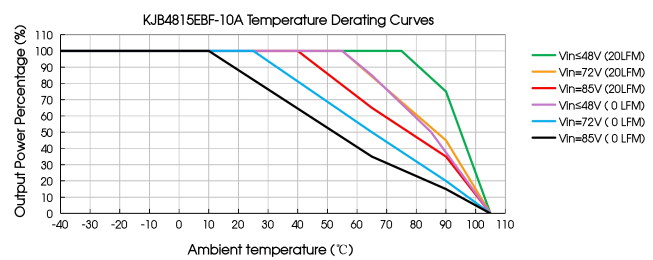
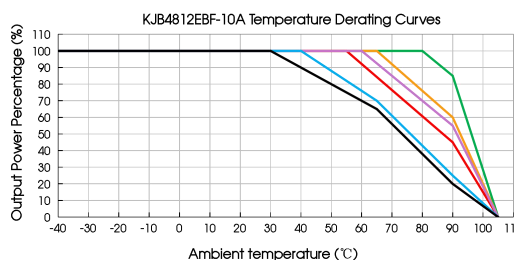
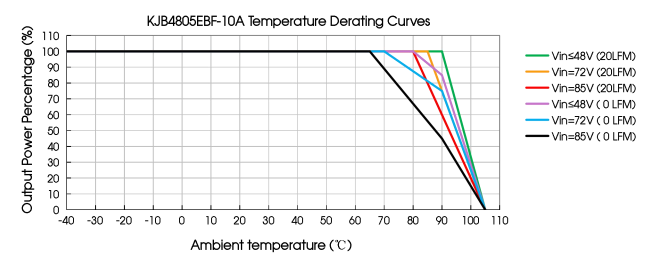
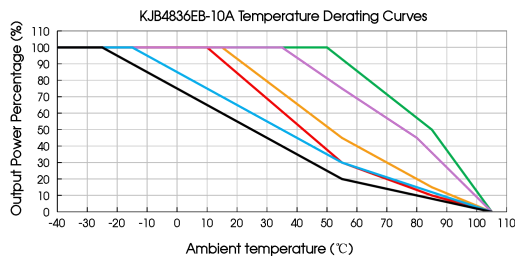
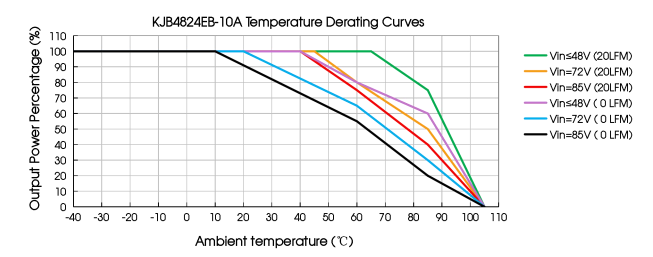
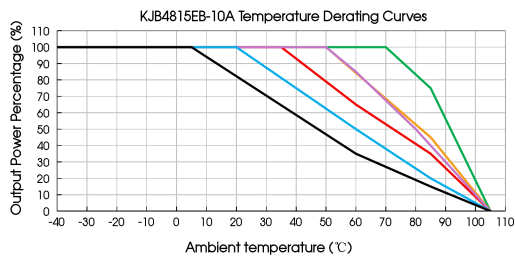
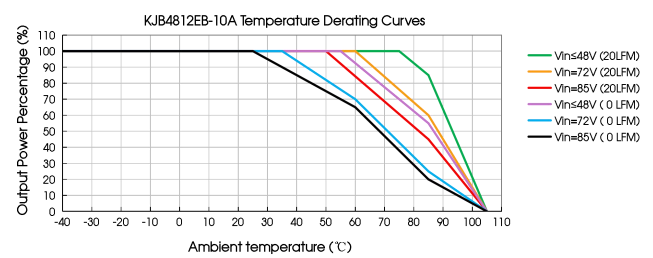
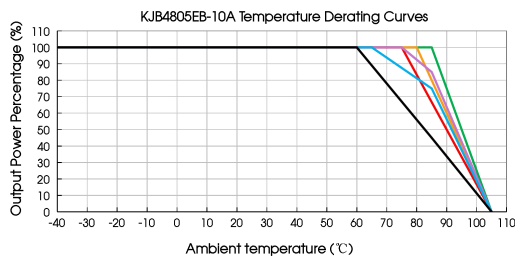
Mechanical Specifications

Case Material	Aluminum alloy	
Dimensions	KJB48xxEB-10A	60.80 x 25.00 x 12.70 mm
	KJB48xxEBF-10A	60.80 x 36.83 x 12.70 mm
Weight	KJB48xxEB-10A	53.0g(Typ.)
	KJB48xxEBF-10A	58.2g(Typ.)
Cooling Method	Free air convection or forced convection	

Electromagnetic Compatibility (EMC)

Emissions	CE	CISPR32/EN55032	CLASS A (see Fig. 3 for recommended circuit)	
	RE	CISPR32/EN55032	CLASS A (see Fig. 3 for recommended circuit)	
Immunity	ESD	IEC/EN61000-4-2	Contact $\pm 6\text{kV}$, Air $\pm 8\text{kV}$	perf. Criteria B
	RS	IEC/EN61000-4-3	10V/m	perf. Criteria A
	EFT	IEC/EN61000-4-4	$\pm 2\text{kV}$ (see Fig. 3 for recommended circuit)	perf. Criteria A
	Surge	IEC/EN61000-4-5	line to line $\pm 2\text{kV}$ (see Fig. 3 for recommended circuit)	perf. Criteria B
	CS	IEC/EN61000-4-6	10Vr.m.s	perf. Criteria A

Typical Characteristic Curves



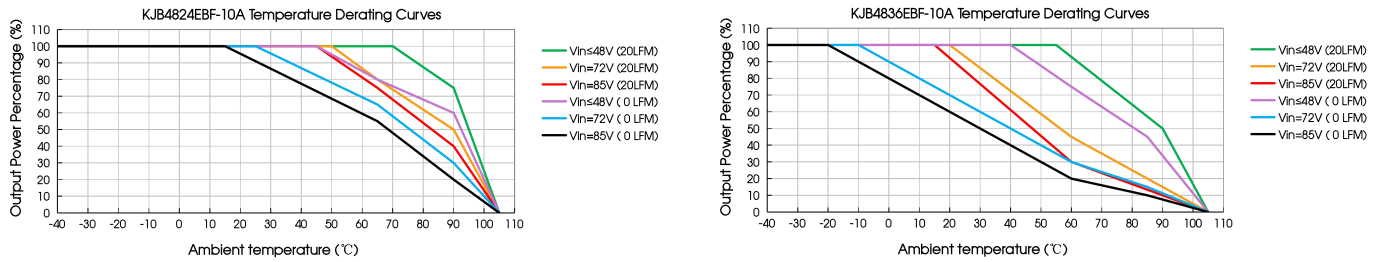
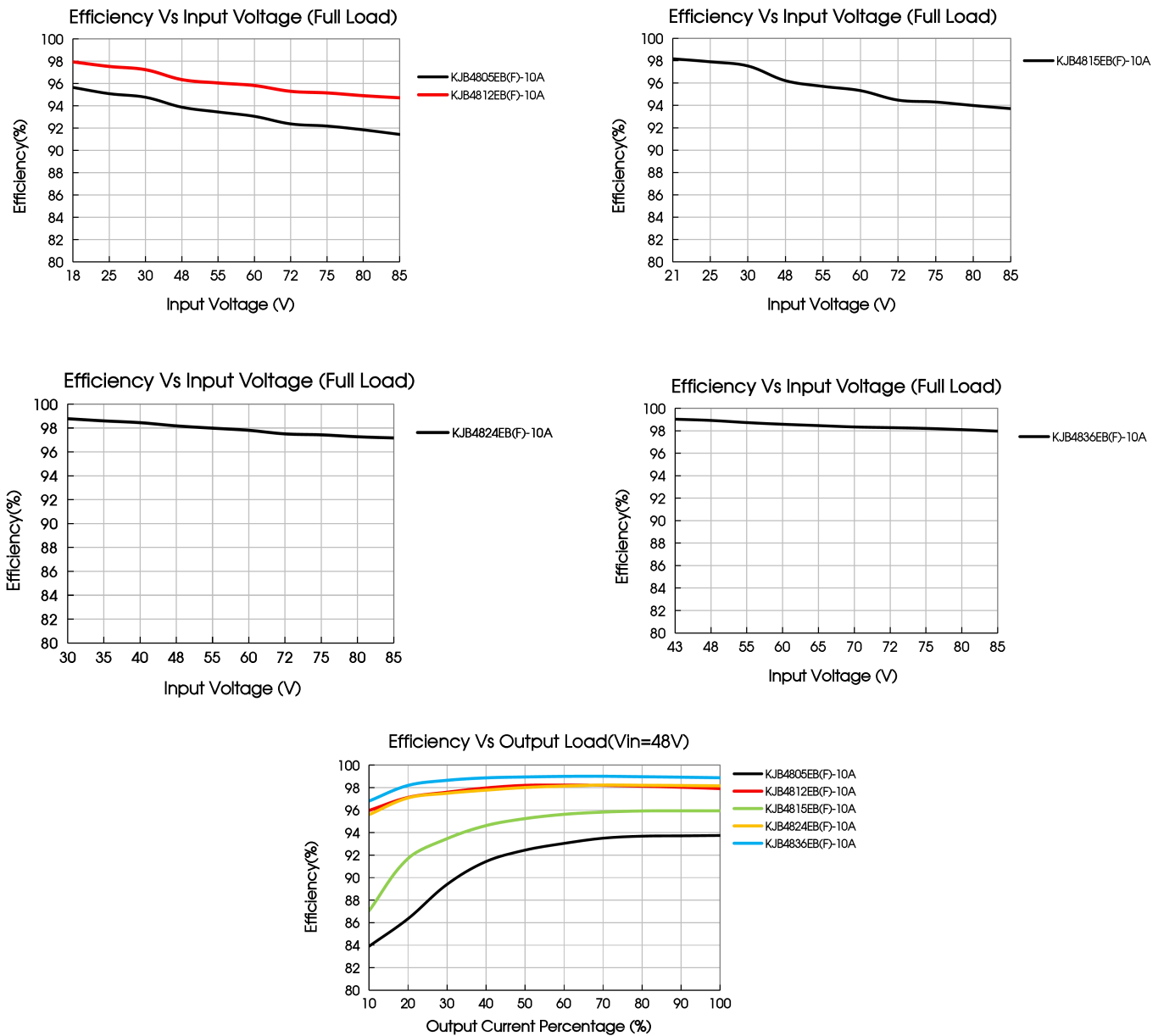
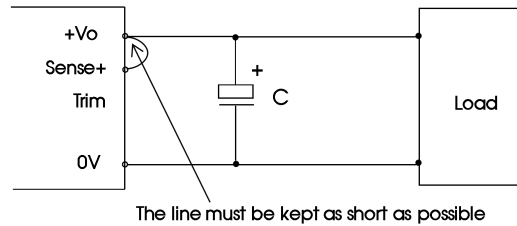


Fig. 1



Remote Sense Application

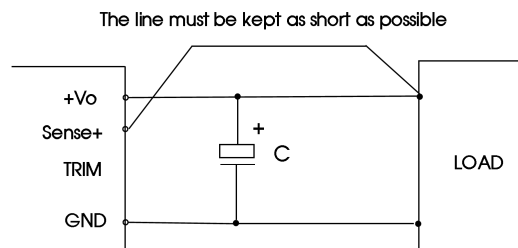
1. Remote Sense Application:



Notes:

- 1) If the sense function is not used for remote regulation the user must connect the Sense+ to +Vo at the DC-DC converter pins and will compensate for voltage drop across pins only.
- 2) The connections between Sense lines and their respective power lines must be kept as short as possible, otherwise they may be picking up noise, interference and/or causing unstable operation of the power module.

2. Remote Sense Connection used for Compensation



Notes:

- 1) Using remote sense with long wires may cause unstable output, please contact technical support if long wires must be used;
- 2) We recommend using adequate cross section for PCB-track layout and/or cables to connect the power supply module to the load in order to keep the voltage drop below 0.3V and to make sure the power supply's output voltage remains within the specified range;
- 3) Note that large wire impedance may cause oscillation of the output voltage and/or increased ripple. Consult technical support or factory for further advice of sense operation.

Design Reference

1. Application circuit

- (1) During product testing and application, please follow the recommended test circuit (Fig. 2); At least one electrolytic capacitor C_{in} is guaranteed to be connected externally to suppress the possible input surge voltage; An external inductor and electrolytic capacitor are connected to output for output filtering;
- (2) If the input terminal of the product is connected in parallel with a circuit with large transient energy (such as a parallel motor drive circuit), the input voltage of the product may be pulled down. At this time, pay attention to the fluctuation of the input voltage of the product, and it is recommended to appropriately increase the capacitance of the electrolytic capacitor C_{in} at the input terminal to ensure the stability of the input terminal voltage and avoid the situation where the input voltage is lower than the under-voltage protection point and cause the product to restart repeatedly;
- (3) If the input and output ripple needs to be further reduced, C_{in} and C_{out} capacity of external capacitors can be appropriately increased or external capacitors with small series equivalent impedance can be selected.

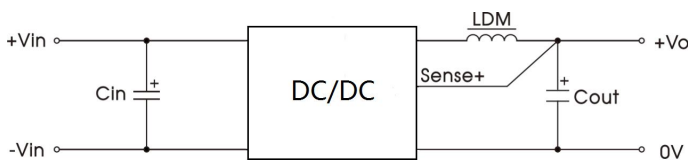


Fig. 2

C_{in}	LDM	C_{out}
330 μ F/100V(ESR<45m Ω)	0.47 μ H/27A	330 μ F/50V

2. EMC compliance circuit

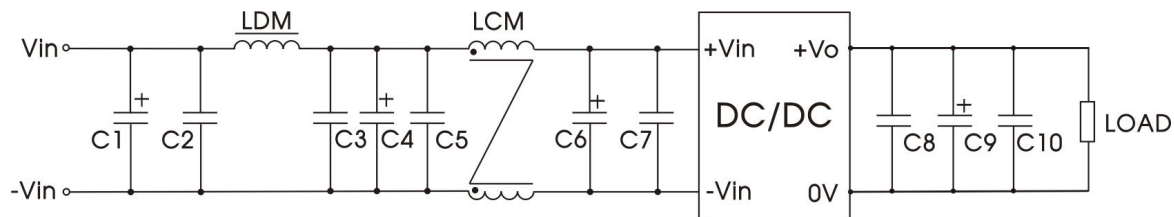
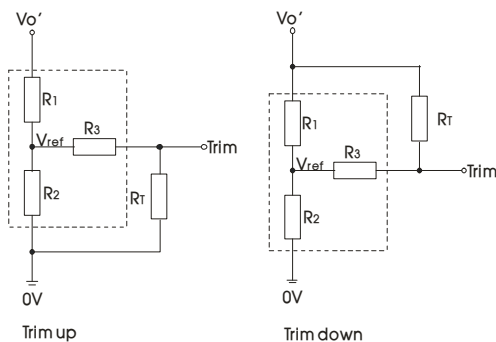


Fig. 3

Components	Recommended Component Value	Components Function
C1, C6	1000μF/100V electrolytic capacitor	Meet EFT and Surge
	330μF/100V electrolytic capacitor	Meet CE and RE
C4	330μF/100V electrolytic capacitor	Universal
C2, C3, C5, C7	4.7μF/100V ceramic capacitor	
LDM	8.2μH/17A	
LCM	FL2D-D0-040 (MORNSUN)	
C8, C10	4.7μF/50V ceramic capacitor	
C9	330μF/50V electrolytic capacitor	

3. Trim Function for Output Voltage Adjustment



Trim resistance calculation formula:

$$\begin{aligned} \text{up: } R_T &= \frac{\alpha R_2}{R_2 - \alpha} - R_3 & \alpha &= \frac{V_{ref}}{V_{o'} - V_{ref}} \cdot R_1 \\ \text{down: } R_T &= \frac{\alpha R_1}{R_1 - \alpha} - R_3 & \alpha &= \frac{V_{o'} - V_{ref}}{V_{ref}} \cdot R_2 \end{aligned}$$

R_T : the Trim resistor

α : a user-defined parameter and has no actual meaning

$V_{o'}$: the actual up or down voltage required

Trim resistor connection (dashed line shows internal resistor network)

Part No.	$R_1(k\Omega)$	$R_2(k\Omega)$	$R_3(k\Omega)$	$V_{ref}(V)$
KJB4805EB(F)-10A	6.8	2.87	12.4	1.5
KJB4812EB(F)-10A	20	2.87	14.7	1.5
KJB4815EB(F)-10A	25.80	2.87	17.5	1.5
KJB4824EB(F)-10A	43.13	2.87	17	1.5
KJB4836EB(F)-10A	83.08	3.6	20.8	1.5

When trimming is used, if the R_T resistor is too small or the Trim and +Vo pins are directly short-circuited, resulting in the output voltage after trimming, the product may be irreparably damaged.

4. The products do not support parallel connection of their output

5. Recommended solution for thermal test

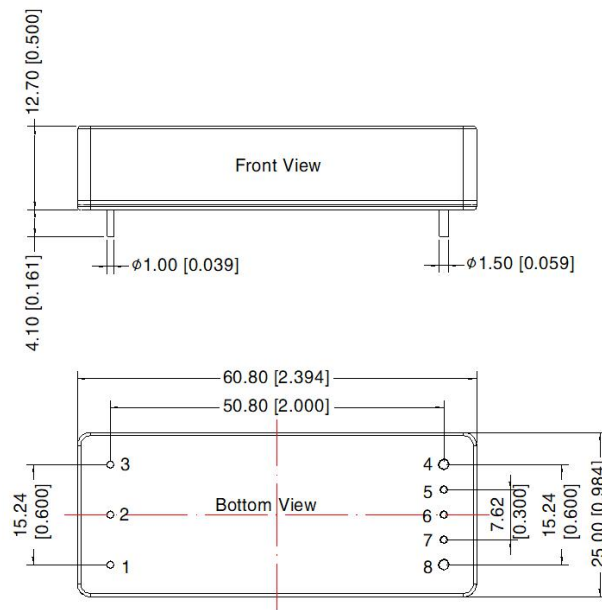
In the application process, the product temperature derating curve can be combined to evaluate the product thermal design; The temperature of point A is used to determine the stable operating range of the product, when it is lower than 110℃, it is the stable operating range.



6. For additional information please refer to DC-DC converter application notes on

www.mornsun-power.com

KJB48xxEB-10A Dimensions and Recommended Layout



Note:

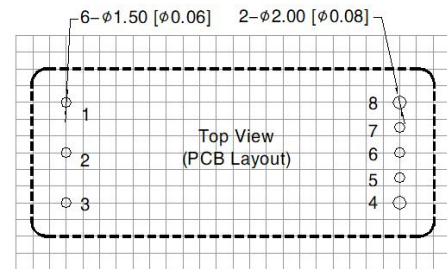
Unit: mm[inch]

Pin1,2,3,6,7 diameter: 1.00[0.039]

Pin4,8 diameter: 1.50[0.059]

Pin diameter tolerances: $\pm 0.10[\pm 0.004]$ General tolerances: $\pm 0.50[\pm 0.020]$

THIRD ANGLE PROJECTION

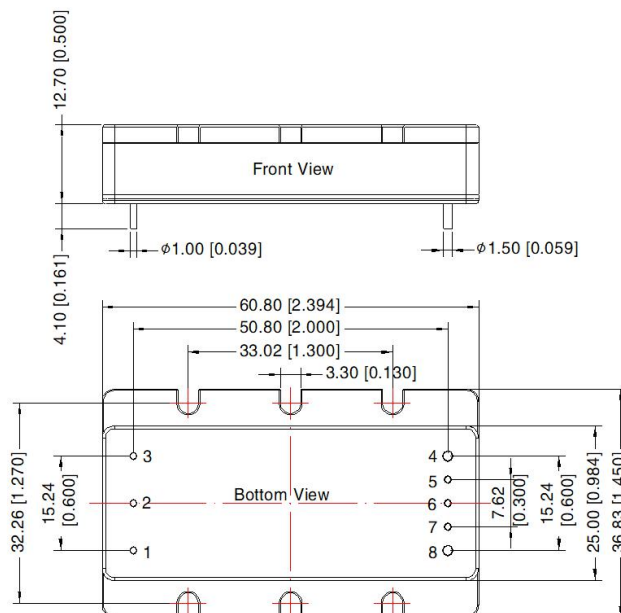


Note: Grid 2.54*2.54mm

Pin-Out

Pin	Mark	Pin	Mark
1	+Vin	5	No pin
2	Ctrl	6	Trim
3	-Vin	7	Sense+
4	0V	8	+Vo

KJB48xxEBF-10A Dimensions and Recommended Layout



Note:

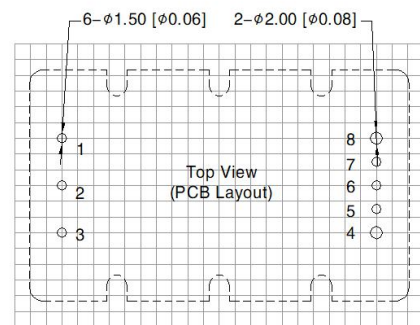
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Pin-Out

Pin	Mark	Pin	Mark
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2	Ctrl	6	Trim
3	-Vin	7	Sense+
4	0V	8	+Vo

Notes:

1. For additional information on Product Packaging please refer to www.mornsun-power.com. Packaging bag number: 58010113;
2. The maximum capacitive load offered were tested at nominal input voltage and full load;
3. Unless otherwise specified, parameters in this datasheet were measured under the conditions of $T_a=25^{\circ}\text{C}$, humidity<75%RH with nominal 48VDC input voltage and rated output load;
4. All index testing methods in this datasheet are based on our company corporate standards;
5. We can provide product customization service, please contact our technicians directly for specific information;
6. Products are related to laws and regulations: see "Features" and "EMC";
7. Our products shall be classified according to ISO14001 and related environmental laws and regulations, and shall be handled by qualified units.

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